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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Tseng Docket No.: TSM03-0763
Serial No.: 10/828,884 Art Unit: 2817
Filed: April 21, 2004 Examiner: TBD
For: Compensated Linearity Voltage-Control-Capacitor Device By Standard CMOS Process

Certificate of Mailing via First Class Mail (37 C.F.R. § 1.8(a))

Date of Deposit: October 31, 2005

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Respectfully submitted,

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